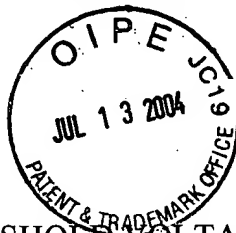




IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Jean-Luc Pelloie
SERIAL NO.: 09/890,120
FILING DATE: July 27, 2001
TITLE: DYNAMIC THRESHOLD VOLTAGE MOS TRANSISTOR
FITTED WITH A CURRENT LIMITER

EXAMINER: Ortiz, Edgardo
ART UNIT: 2815



CERTIFICATE OF MAILING

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APPLICANT'S COMMENTS ON REASONS FOR ALLOWANCE

These comments are responsive to the Notice of Allowance mailed on
April 23, 2004, which sets forth the Examiner's statement of reasons for allowance.

The Examiner is thanked for the kind allowance of all pending claims (i.e. claims 1-11). Applicants acknowledge the Examiner's statement of reasons for allowance as set forth in the Notice of Allowance. However, Applicants point out that the reasons for allowability of the above referenced claims are not limited to the reasons for allowance as set forth in the Notice of Allowance, and that additional reasons for allowability may

exist, each of which may be independently sufficient to establish the patentability of one or more pending claims.


Applicants respectfully reserve the right to introduce, articulate, or otherwise comment on any such additional reasons for allowance as may be appropriate in any future proceedings concerning the claimed invention.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-1698.

Respectfully submitted,
THELEN REID & PRIEST LLP

Dated:

July 8, 2009


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